

IN THE CLAIMS

The following is a complete listing of the claims, and replaces all earlier versions and listings.

1. (original) An unauthorized access prevention method for an integrated circuit comprising one or plural resistor elements capable of selecting between a high impedance state and a low impedance state irreversibly in an interface portion within the integrated circuit or a peripheral circuit portion,

wherein, when a signal inconsistent with verification information and standard that are preset in the integrated circuit is received at least once, the impedance state of the resistor element is changed from an initial state to stop a part or all of accesses to the integrated circuit irreversibly.

2. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein the resistor element contains an organic conductor.

3. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein the resistor element is formed of a capacitor.

4. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein a voltage higher than at normal operation is applied to the resistor element in order to change its impedance.

5. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein a current larger than at normal operation is applied to the resistor element in order to change its impedance.

6. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein the verification information and standard that are preset in the integrated circuit contain a keyword or a logic.

7. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein the verification information and standard that are preset in the integrated circuit contain a clock frequency different from that in a specification.

8. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein the verification information and standard that are preset in the integrated circuit contain a power supply voltage different from that in a specification.

9. (original) An unauthorized access prevention method for an integrated circuit as claimed in claim 1, wherein the integrated circuit contain an organic semiconductor.

10. (currently amended) An IC card which uses the unauthorized access prevention method of ~~any one of claim[[s]] 1 to 9~~.

11. (new) An IC card which uses the unauthorized access prevention method of claim 2.

12. (new) An IC card which uses the unauthorized access prevention method of claim 3.

13. (new) An IC card which uses the unauthorized access prevention method of claim 4.

14. (new) An IC card which uses the unauthorized access prevention method of claim 5.

15. (new) An IC card which uses the unauthorized access prevention method of claim 6.

16. (new) An IC card which uses the unauthorized access prevention method of claim 7.

17. (new) An IC card which uses the unauthorized access prevention method of claim 8.

18. (new) An IC card which uses the unauthorized access prevention method of claim 9.